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1  '
2  '  Firmware-Patch fuer CHAIN-MERGE
3  '  Schneider CPC & DDI-1
4  '
5  '  CPC-INFO  ESCON
6  '

10 MEMORY HIMEM-41
20 DEF FNmsb(a)=&FF AND INT(a/256)
30 DEF FNlsb(a)=&FF AND UNT(a)
40 FOR i=HIMEM+1 TO HIMEM+38
50 READ byte
60 POKE i,byte
70 NEXT i
80 POKE HIMEM+ 3, FNlsb(HIMEM+39)
90 POKE HIMEM+ 4, FNmsb(HIMEM+39)
100 POKE HIMEM+ 9, FNlsb(HIMEM+41)
110 POKE HIMEM+10, FNmsb(HIMEM+41)
120 POKE HIMEM+18, FNlsb(HIMEM+ 1)
130 POKE HIMEM+19, FNmsb(HIMEM+ 1)
140 REM CAS_IN_CHAR
150 POKE HIMEM+39, PEEK(&BC80+0)
160 POKE HIMEM+40, PEEK(&BC80+1)
170 POKE HIMEM+41, PEEK(&BC80+2)
180 POKE &BC80+ 0, &C3
190 POKE &BC80+ 1, FNlsb(HIMEM+1)
200 POKE &BC80+ 2, FNmsb(HIMEM+1)
210 DATA &e5,&2a,&00,&00,&22,&80,&bc
220 DATA &3a,&00,&00,&32,&82,&bc
230 DATA &cd,&80,&bc,&21,&00,&00
240 DATA &22,&81,&bc,&21,&80,&bc
250 DATA &36,&c3,&e1,&d8,&c8,&fe,&1a
260 DATA &37,&3f,&c0,&b7,&37,&c9

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